

Claims

- [c1] 1. An integrated control chip with a power control for a DVD player, said DVD player including an MPEG system, a power control circuit and a VFD, said chip comprising: an MPEG interface between said MPEG system and said integrated control chip, said MPEG interface sending a starting data to said integrated control chip; a VFD drive circuit, coupled to said MPEG interface, driving said VFD; an external control signal receiver receiving an external control signal; a starter control circuit, coupled to said external control signal receiver, determining whether said power control circuit turns on said DVD player based on said external control signal; and a memory device, coupled to said MPEG interface and said starter control circuit, storing said starting data so that said starter control circuit reads said starting data.
- [c2] 2. The chip of claim 1, further comprising an external starting data interface coupled to a starting data provider, said memory device reading said starting data defined by an external source via said external starting

data interface.

- [c3] 3. The chip of claim 2, wherein when said memory device is not stored with said starting data, said memory device reads said starting data provided by said starting data provider via said external starting data interface and stores said starting data provided by said starting data provider, and when said starting data is not read by said memory device through said external starting data interface in a predetermined period, said memory device reads said starting data provided by said MPEG system and stores said starting data provided by said MPEG system.
- [c4] 4. The chip of claim 2, wherein said starting data provider is an EEPROM.
- [c5] 5. The chip of claim 1, wherein said external control signal receiver receives an external control signal from an external controller and sends said external control signal to said starter control circuit and said MPEG interface.
- [c6] 6. The chip of claim 5, wherein said starter control circuit controls said power control circuit in response to said starting data stored in said memory device and said external control signal.

- [c7] 7.The chip of claim 5, wherein said external controller comprises
a keyboard allowing a user to input said external control signal; and
a remote controller inputting said external control signal via an infrared ray signal.
- [c8] 8.The chip of claim 7, wherein said external control signal receiver comprises
a keyboard detector detecting signals sent by said keyboard; and
an infrared ray receiver receiving said infrared ray signal sent by said remote controller.
- [c9] 9.The chip of claim 1, wherein said starter control circuit comprises a logic circuit.
- [c10] 10. An integrated control chip with a power control for a DVD player, said DVD player including a power control circuit and an external controller, said chip comprising:
an external control signal receiver receiving an external control signal from said external controller;
a starter control circuit, couple to said external control signal receiver, determining whether said power control circuit turns on said DVD player based on said external control signal;

an MPEG interface between an MPEG system and an integrated control chip, said MPEG interface sending a starting data to said integrated control chip;
a memory device coupled to said MPEG interface and said starter control circuit, storing said starting data so that said MPEG interface reads said starting data; and
an external starting data interface coupled to a starting data provider, said memory device reading said starting data defined by an external source via said external starting data interface.

[c11] 11. The chip of claim 10, further comprising:

a display drive circuit, coupled to said MPEG interface, driving said VFD.

[c12] 12. The chip of claim 10, wherein when said memory device is not stored with said starting data, said memory reads said starting data provided by said starting data provider via said external starting data interface and stores said starting data provided by said starting data provider, and when said memory device does not read said starting data through said external starting data interface in a predetermined period, said memory device reads said starting data provided by said MPEG system and stores said starting data provided by said MPEG system, and said DVD player enters into a standby mode.

- [c13] 13. The chip of claim 10, wherein said starting data provider is an EEPROM.
- [c14] 14. The chip of claim 10, wherein said external control signal receiver receives said external control signal from said external controller and sends said external control signal to said starter control circuit and said MPEG interface.
- [c15] 15. The chip of claim 14, wherein said starter control circuit controls said power control circuit in response to said starting data stored in said memory and said external control signal.
- [c16] 16. The chip of claim 14, wherein said external controller comprises
a keyboard allowing a user to input said external control signal; and
a remote controller inputting said external control signal via an infrared ray signal.
- [c17] 17. The chip of claim 14, wherein said external control signal receiver comprises
a keyboard detector detecting signals sent by said keyboard; and
an infrared ray receiver receiving said infrared ray signal sent by said remote controller.

[c18] 18. The chip of claim 10, wherein said starter control circuit comprises a logic circuit.